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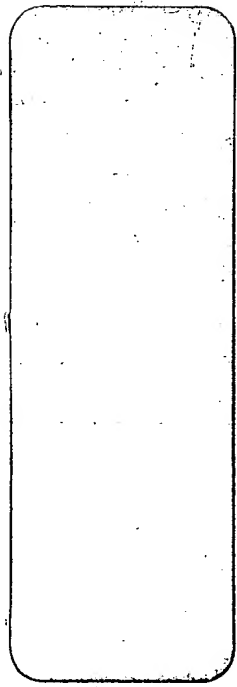
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NOTICE OF ALLOWANCE AND FEE(S) DUE

7590 05/18/2005
SCOTT C. HARRIS
Fish & Richardson P.C.
Suite 500
4350 La Jolla Village Drive
San Diego, CA 92122

EXAMINER	
BOUTSIKARIS, LEONIDAS	
ART UNIT	PAPER NUMBER
2872	
DATE MAILED: 05/18/2005	

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/965,033	09/25/2001	Yu Wong	06618-201002	3226

TITLE OF INVENTION: DEVICES BASED ON AN ARRAY OF LIGHT-FILTERING CHANNELS WITH SURFACE PLASMON INTERFERENCE FILTERS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	08/18/2005

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. **PROSECUTION ON THE MERITS IS CLOSED.** THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN **THREE MONTHS** FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. **THIS STATUTORY PERIOD CANNOT BE EXTENDED.** SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE REFLECTS A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE APPLIED IN THIS APPLICATION. THE PTOL-85B (OR AN EQUIVALENT) MUST BE RETURNED WITHIN THIS PERIOD EVEN IF NO FEE IS DUE OR THE APPLICATION WILL BE REGARDED AS ABANDONED.

HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

- A. If the status is the same, pay the TOTAL FEE(S) DUE shown above.
- B. If the status above is to be removed, check box 5b on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and twice the amount of the ISSUE FEE shown above, or

If the SMALL ENTITY is shown as NO:

- A. Pay TOTAL FEE(S) DUE shown above, or
- B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and 1/2 the ISSUE FEE shown above.

II. PART B - FEE(S) TRANSMITTAL should be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). Even if the fee(s) have already been paid, Part B - Fee(s) Transmittal should be completed and returned. If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

PART B - FEE(S) TRANSMITTAL

Complete and send this form, together with applicable fee(s), to: **Mail**

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INSTRUCTIONS: This form should be used for transmitting the ISSUE FEE and PUBLICATION FEE (if required). Blocks 1 through 5 should be completed where appropriate. All further correspondence including the Patent, advance orders and notification of maintenance fees will be mailed to the current correspondence address as indicated unless corrected below or directed otherwise in Block 1, by (a) specifying a new correspondence address; and/or (b) indicating a separate "FEE ADDRESS" for maintenance fee notifications.

CURRENT CORRESPONDENCE ADDRESS (Note: Use Block 1 for any change of address)

7590 05/18/2005

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Note: A certificate of mailing can only be used for domestic mailings of the Fee(s) Transmittal. This certificate cannot be used for any other accompanying papers. Each additional paper, such as an assignment or formal drawing, must have its own certificate of mailing or transmission.

Certificate of Mailing or Transmission

I hereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Mail Stop ISSUE FEE address above, or being facsimile transmitted to the USPTO (703) 746-4000, on the date indicated below.

(Depositor's name)
(Signature)
(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	08/18/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
BOUTSIKARIS, LEONIDAS	2872	359-585000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).
☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
☐ "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer Number is required.

2. For printing on the patent front page, list
 (1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
 (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.
 1 _____
 2 _____
 3 _____

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE (B) RESIDENCE: (CITY and STATE OR COUNTRY)

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☐ Corporation or other private group entity ☐ Government

4a. The following fee(s) are enclosed:

☐ Issue Fee
☐ Publication Fee (No small entity discount permitted)
☐ Advance Order - # of Copies _____

4b. Payment of Fee(s):

☐ A check in the amount of the fee(s) is enclosed.
☐ Payment by credit card. Form PTO-2038 is attached.
☐ The Director is hereby authorized by charge the required fee(s), or credit any overpayment, to Deposit Account Number _____ (enclose an extra copy of this form).

5. Change in Entity Status (from status indicated above)

☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

The Director of the USPTO is requested to apply the Issue Fee and Publication Fee (if any) or to re-apply any previously paid issue fee to the application identified above.

NOTE: The Issue Fee and Publication Fee (if required) will not be accepted from anyone other than the applicant; a registered attorney or agent; or the assignee or other party in interest as shown by the records of the United States Patent and Trademark Office.

Authorized Signature _____

Date _____

Typed or printed name _____

Registration No. _____

This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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09/965,033	09/25/2001	Yu Wong	06618-201002	3226

7590 05/18/2005

EXAMINER

BOUSIKARIS, LEONIDAS

ART UNIT	PAPER NUMBER
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2872

DATE MAILED: 05/18/2005

SCOTT C. HARRIS
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San Diego, CA 92122

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b) (application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 864 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 864 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (<http://pair.uspto.gov>).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571) 272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at (703) 305-8283.

Notice of Allowability

Application No.

09/965,033

Examiner

Leo Boutsikaris

Applicant(s)

WONG, YU

Art Unit

2872

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to amendment filed on 9/5/02.
2. ☒ The allowed claim(s) is/are 1-12.
3. ☒ The drawings filed on 25 September 2001 are accepted by the Examiner.
4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).
 - * Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- | | |
|---|---|
| 1. <input type="checkbox"/> Notice of References Cited (PTO-892) | 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 6. <input type="checkbox"/> Interview Summary (PTO-413),
Paper No./Mail Date _____ |
| 3. <input type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____ | 7. <input checked="" type="checkbox"/> Examiner's Amendment/Comment |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit
of Biological Material | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance |
| | 9. <input type="checkbox"/> Other _____ |

EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Bing Ai (Reg. No. 43,312) on 5/12/2005.

The application has been amended as follows:

IN THE CLAIMS

Claim 1 is rewritten as follows:

1. A device, comprising:

a plurality of layers stacked over one another to form a layered structure which supports a two-dimensional array of light-filtering channels, wherein each layer is structured to have an one-dimensional array of light-filtering channels,

[an] wherein the two-dimensional array of light-filtering channels [having] comprises an input surface from which said light-filtering channels receive input light and an output surface from which said light-filtering channels export output light, and wherein each light-filtering channel comprises:

a light-conducting channel formed of a transparent dielectric material having a first surface which is substantially reflective and a second surface opposing said first

Art Unit: 2872

surface, said first and second surfaces substantially parallel to said light-conducting channel; and

at least two optical filters sequentially formed on said second surface along said light-conducting channel to reflect said input light between said first and second surfaces so that said input light is sequentially reflected and filtered by said optical filters to produce said output light, wherein each optical filter includes at least one metal layer and an electro-optical dielectric layer contacting with each other to form a metal-dielectric interface which generates a surface plasmon wave in response to a p-polarized input light beam to transmit light at a selected wavelength within a bandwidth according to a control voltage from said metal layer to said dielectric layer and reflects light of other wavelengths; and

at least two thin-film transistors respectively formed on said optical filters to provide said control voltage to control a refractive index of said dielectric layer and thereby said selected wavelength to change a color and a gray scale of said output light.

Claim 6 is rewritten as follows:

6. A device, comprising:

a plurality of transparent plates each having a filtering surface and an opposing, reflecting surface, a metallic layer formed over said filtering surface of each transparent plate;

an electro-optical dielectric layer, whose refractive index changes in response to a control voltage, disposed in contact with said metallic layer to form a metal-dielectric interface which generates a surface plasmon wave in response to a [p-polarized] polarized

Art Unit: 2872

input light beam to transmit light through said metallic layer at a selected wavelength within a bandwidth according to a local refractive index of said electro-optical dielectric layer at each location of said metallic layer where light is reflected and to reflect light of other wavelengths back to each transparent plate; and

a plurality of parallel linear arrays of transistors formed over said dielectric layer, wherein said transistors are independent from one another, and where each parallel linear array of transistors defines a light channel along which light is reflected between said filtering and said reflecting surfaces via at least two locations on the filtering surface with two transistors sequentially located along the light channel to modify a color and an intensity of said light according to voltages from said transistors in each linear array relative to a common voltage of said metallic layer,

wherein said transparent plates are stacked over one another so that a reflecting surface of one transparent plate faces a filtering surface of an adjacent transparent plate to form a two-dimensional array of light channels.

Claims 11-16 are cancelled.

The following new claims are added:

17. (New) A method, comprising:

providing a plurality of plates each comprising a dielectric material layer having a first reflective surface and a second, opposing metallic surface;

patterning the second metallic surface to include a two-dimensional array of separate metallic areas, a plurality of metal-dielectric interfaces on each separate metallic

Art Unit: 2872

area, and thin-film transistors above the metal-dielectric interfaces respectively on the separate metallic areas;

stacking and bonding the plates over one another to form a composite structure;
and

slicing the composite structure into a plurality of panels of two-dimensional light-filtering channels where each panel includes a portion of the stacked plates and each portion in each plate includes at least three separate metallic areas as three independent surface filters along a direction that is perpendicular to a direction of the slicing and defines a light-filtering channel wherein light incident to one of the light-filtering channels of the plate is sequentially reflected and filtered by the three surface plasmon filters to produce a filtered optical output on the side of the plate.

18. (New) The method as in claim 17, further comprising:

providing independent electric controls to separate metallic areas in one sliced panel; and

controlling voltages applied to the independent electrical controls to control light intensities and colors of output light from the light-filtering channels for light entering from one side of the panel and exiting on an opposite side of the panel.

The following is an examiner's statement of reasons for allowance:

Claims 1-10, 17-18 are allowable over the prior art of record for at least the reason that even though the prior art discloses a color display panel comprising a metal/dielectric interface for filtering incident light, the prior art fails to teach or

Art Unit: 2872

reasonably suggest, regarding claims 1-5, a device comprising at least two optical filters sequentially formed on the second surface along the light-conducting channel to reflect the input light between the first and second surfaces so that the input light is sequentially reflected and filtered by the optical filters, wherein each optical filter includes a metal-dielectric interface which generates a surface plasmon wave, regarding claims 6-10, a device wherein each parallel linear array of transistors defines a light channel along which light is reflected between the filtering and reflecting surfaces via at least two locations on the filtering surface with two transistors sequentially located along the light channel, and regarding claims 17-18, a method wherein light incident to one of the light-filtering channels of the plate is sequentially reflected and filtered by the three surface plasmon filters to produce a filtered optical output on the side of the plate, as set forth by the claimed combination.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dr. Leo Boutsikaris whose telephone number is 571-272-2308.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status

Art Unit: 2872

information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Leo Boutsikaris, Ph.D., J.D.
Primary Patent Examiner, AU 2872
May 13, 2005



LEONIDAS BOUTSIKARIS
PRIMARY EXAMINER